

FIG. 1

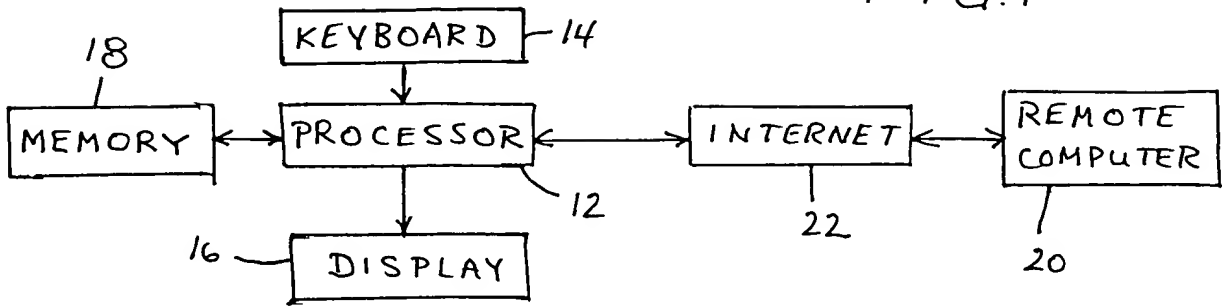


FIG. 2

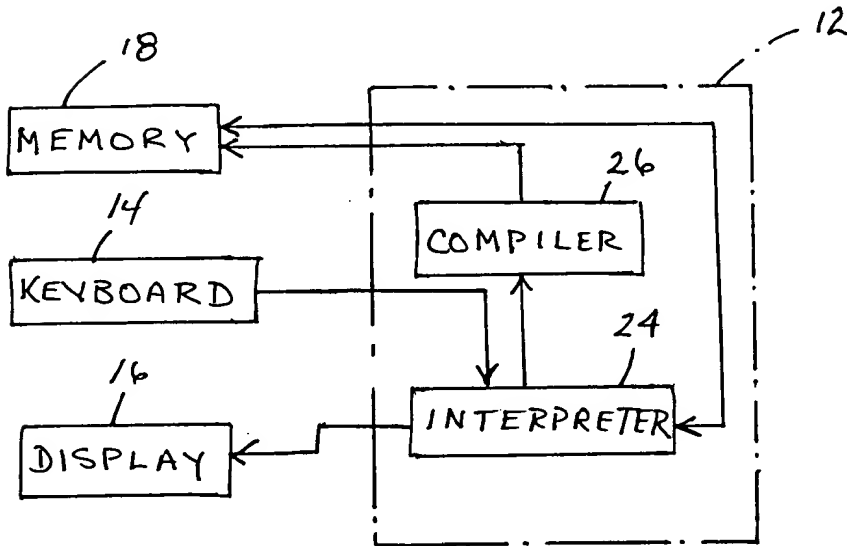
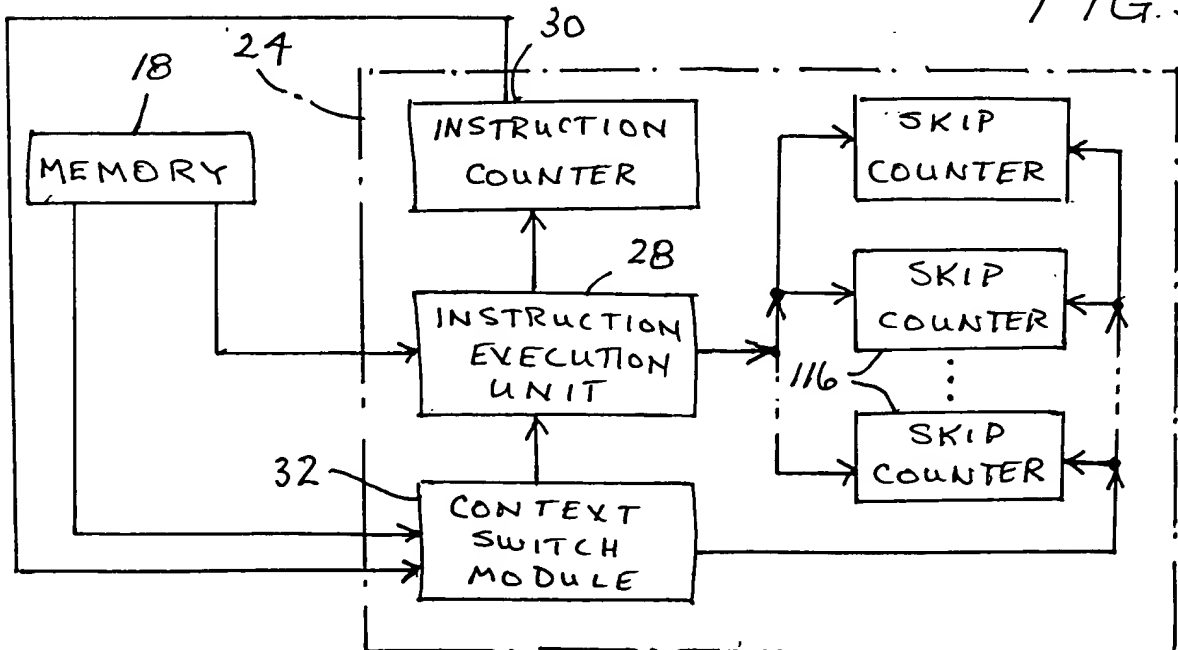


FIG. 3



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FIG. 4 is a block diagram of a system architecture for managing threads.

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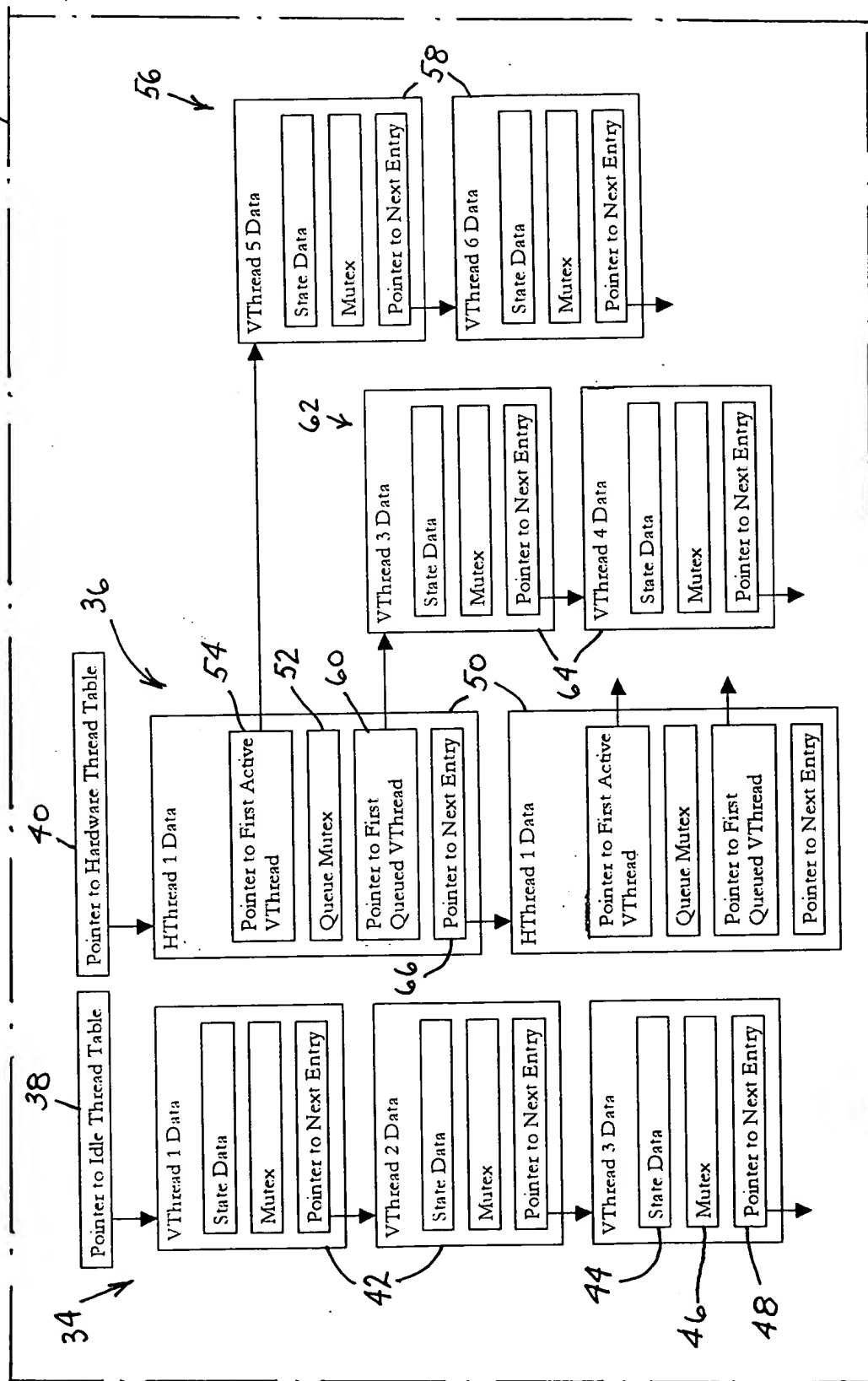


FIG. 4

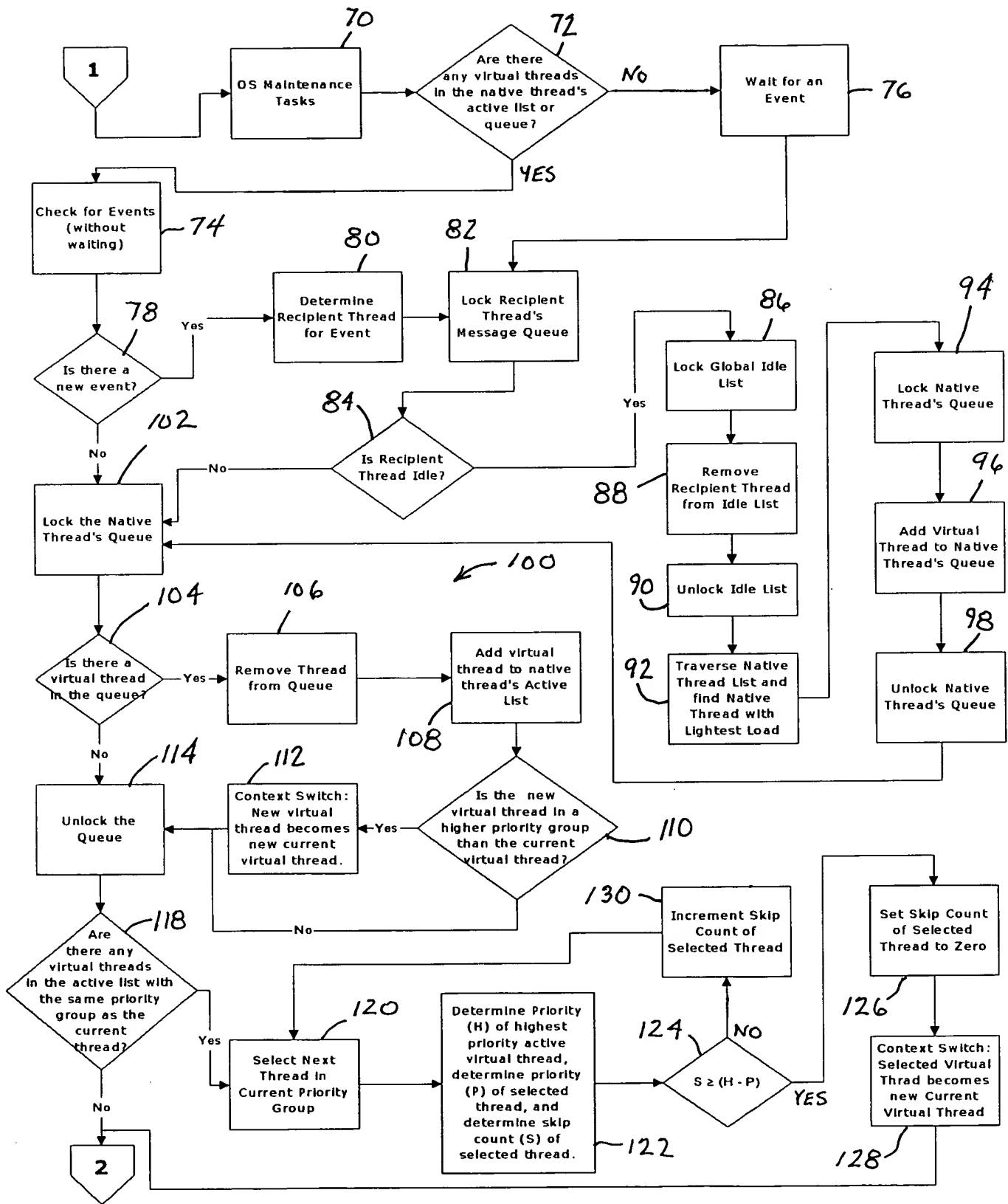


FIG. 5A

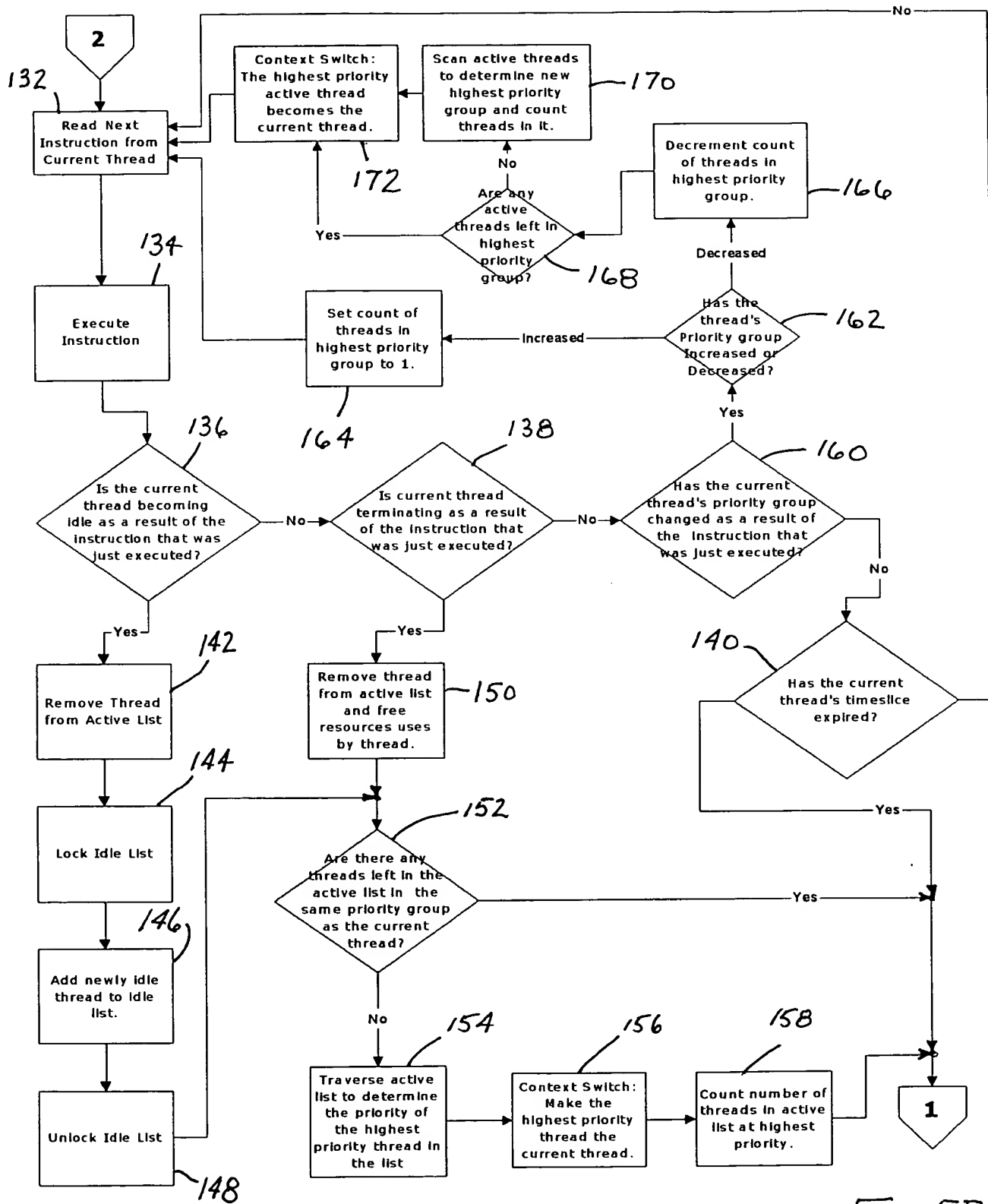


FIG. 5B

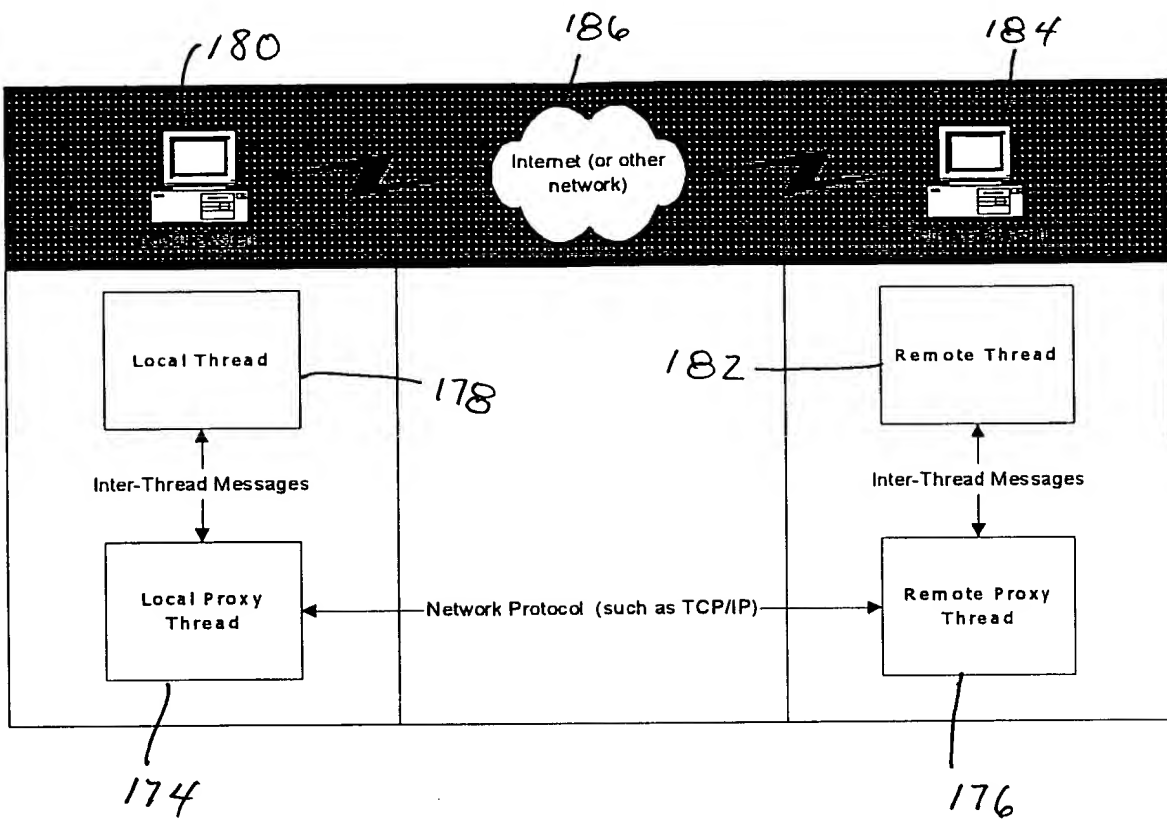


FIG. 6